

<b>Institute Name</b>	MAHARAJA AGRASEN INSTITUTE OF TECHNOLOGY
<b>India Rankings 2017</b>	IR17-ENGG-2-12853
<b>Discipline</b>	ENGINEERING

Parameter	Sponsored Research Projects								
2D.FPPP	S.No.	Financial Year	Name of Faculty (Principal Investigator)	Name of the Funding agency	Title of the Project	Sanctioned order no.	Sanctioned date	Amount Received(In Rupees)	Amount received (in words)
	1	2015-16	1. Prof R.S. Gupta	D.R.D.O	Analysis and characterization of Silicon Gate All Around Nanowire MOSFET for ULSI Circuit Application.	ERIP/ER/1002196	26 DEC. 2011	7,39,000	Seven lakh thirty nine thousand only
			2. Prof R.S. Gupta	AICTE	Analytical Modeling Simulation and charavterization of Si gate All around Nanowire MOSFET ULS Integration	8023/RID/RD S/036/2011/12	Aug. 2012	0	NIL
	2	2014-15	1. Prof R.S. Gupta	D.R.D.O	Analysis and characterization of Silicon Gate All Around Nanowire MOSFET for ULSI Circuit Application.	ERIP/ER/1002196	26 DEC. 2011	6,43,338	Six lakh forty three thousand three hundred thirty eight only
			2. Prof R.S. Gupta	AICTE	Analytical Modeling Simulation and charavterization of Si gate All around Nanowire MOSFET ULS Integration	8023/RID/RD S/036/2011/12	Aug. 2012	0	NIL
	3	2013-14	1. Prof R.S. Gupta	D.R.D.O	Analysis and characterization of Silicon Gate All Around Nanowire MOSFET for ULSI Circuit Application.	ERIP/ER/1002196	26 DEC. 2011	15,82,000	Fifteen lakh eighty two thousand only
			2. Prof R.S. Gupta	AICTE	Analytical Modeling Simulation and charavterization of Si gate All around Nanowire MOSFET ULS Integration	8023/RID/RD S/036/2011/12	Aug. 2012	1,30,000	One lakh thirty thousand